SoC Lab4-1

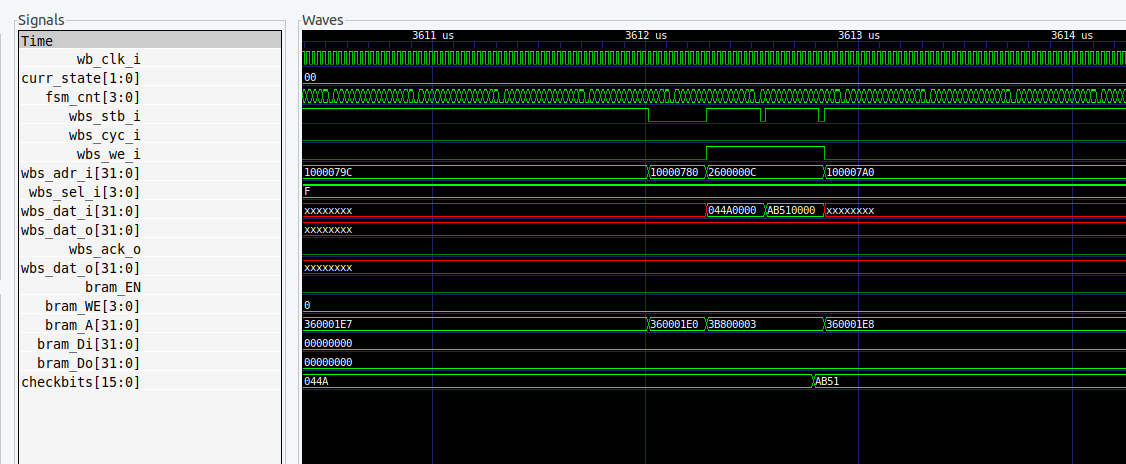
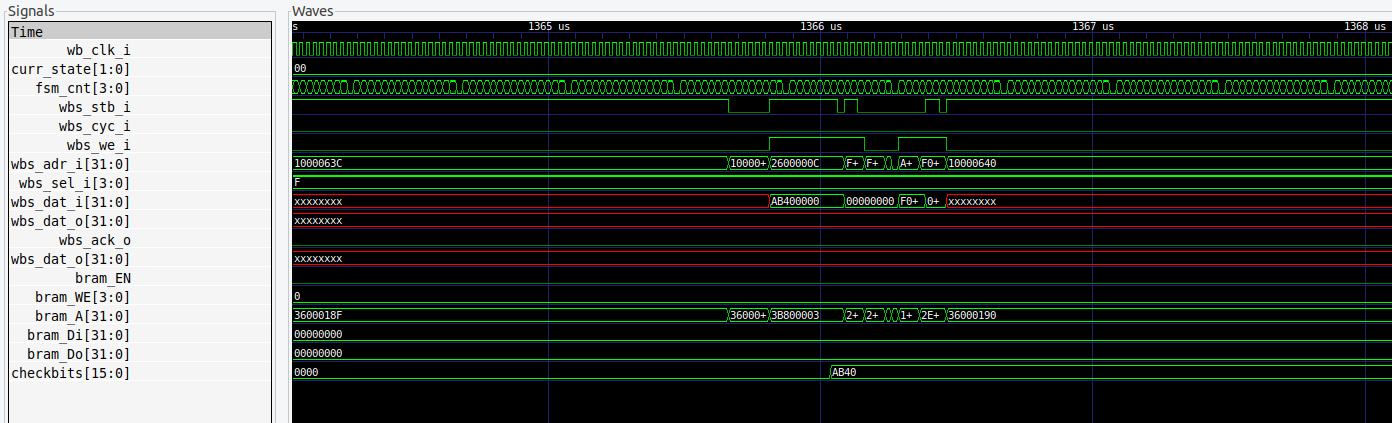
組別: 第十六組 成員:周聖平、蔡以心、張煒侖

1. Explanation of your firmware code

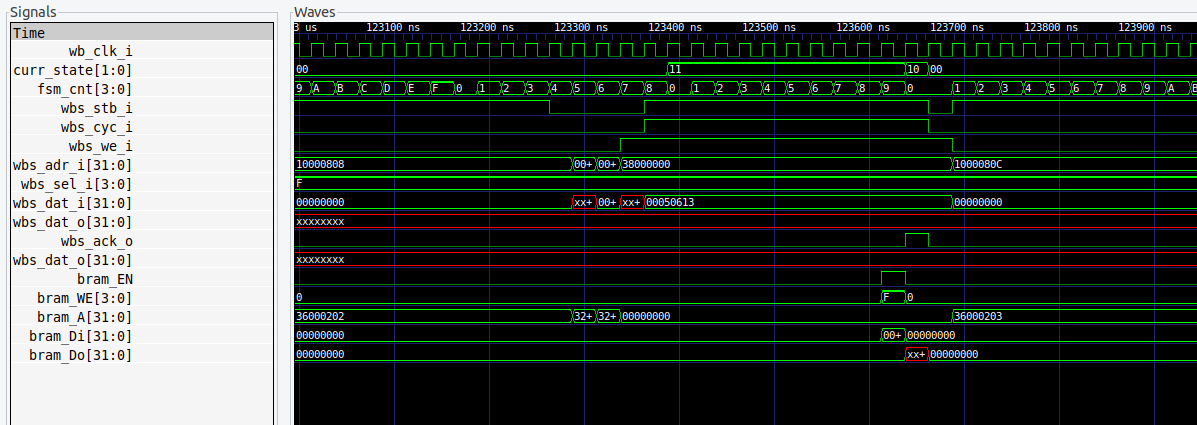
* How does it execute a multiplication in assembly code
* What address allocate for user project and how many space is required to allocate to firmware code

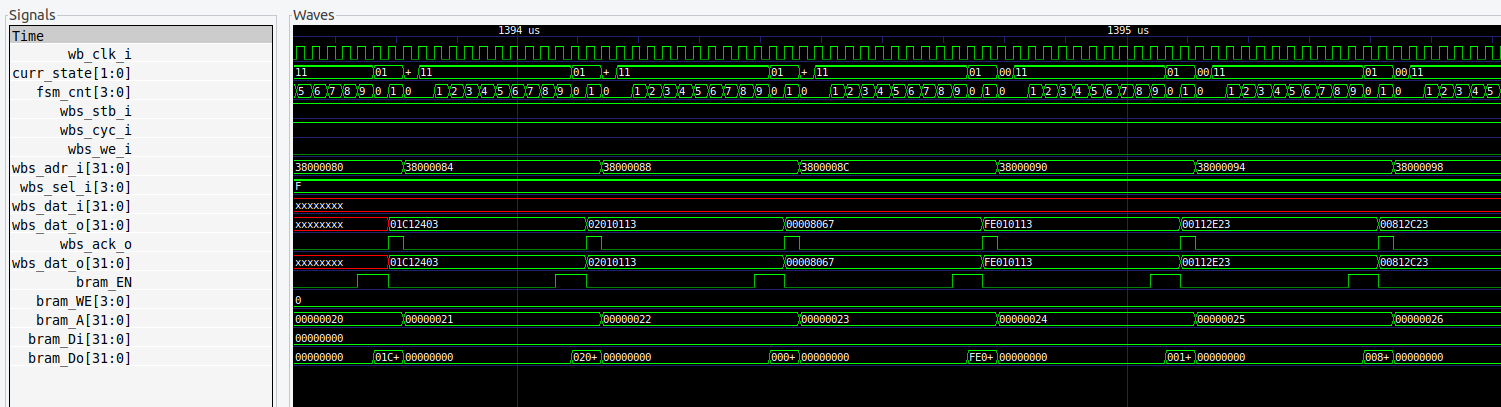
二、Interface between BRAM and wishbone

* Waveform from xsim



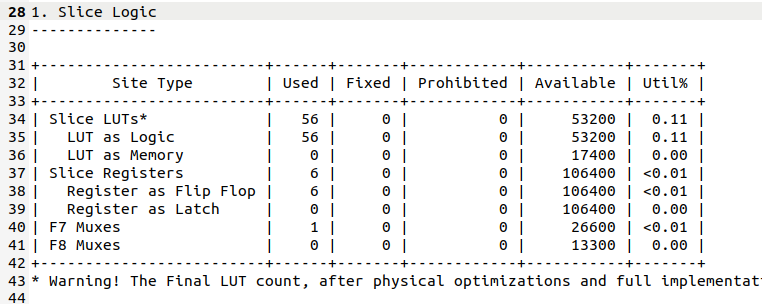
* FSM

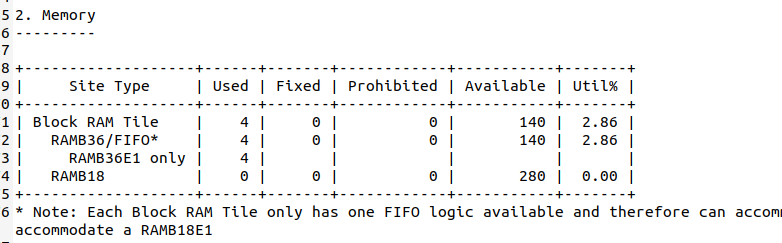




1. Synthesis report

LUT: 56 Register: 6



BRAM:4

四、Other discoveries